SHEET 1 OF 1

		SHEET TOF T				
Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT No. 027260-668	APPLICATION NO. Unassigned 10/615,929				
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Takashi TOYODA et al.					
	FILING DATE July 10, 2003	GROUP Unassigned 2818				

		ţ	JIS. PATENT DOCUMENTS		famile.			
		Kind Code (if known)			1	Issue/Publication Date (MM-DD-YYYY)		
6,060,765		Maeda			05-	05-09-2000		
1								
<u> </u>								
<u> </u>								
<u> </u>								
					 			
								
					-			
					_			
100	140	FO	REIGN PATENT DOCUMENTS					
Examiner			Country	Date of Publication (MM-DD-YYYY)		Translatio		
Initials	2002-83949	(if known)	Country Japan	03-22-20		Abst	~	
2	2002-00343			00-22-20	-	٨		
							_	
		NONTRA	THENT LETERATURE DOCUMENTS		- P	17	7 1	
			(In CAPITAL LETTERS), title of the article (who		reside a ur de d	12	31	
Examiner			nal, serial, symposium, catalog, etc.), date, page	e(s), volume-issue		(s),		
initials	MARRIED EN IDAMAN	A st at State Co	publisher, city and/or country where published institivity and No-Crosstalk Pixel Technology for	l. Embedded CMOS	10000	200001		
	TRANSACTIONS OF	N ELECTRON DE	EVICES, Vol. 48, No. 10, October 2001, pp. 222	1-2227.	illiage s	MINEUI,	160	
······································							_	
	·							

EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.